CIT Case No. 1917
PATENT
JPL Case No. C-16984
Docket No. JPL 87-008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Inventor: Larry A. Bergman

Serial No. 103,192   Group No. 237

Filed: 1 October 1987

For: OPTICALLY INTRACONNECTED COMPUTER EMPLOYING
     DYNAMICALLY RECONFIGURABLE HOLOGRAPHIC OPTICAL
     ELEMENT

Examiner: K. KRIESS

AMENDMENT

Hon. Commissioner of Patents and Trademarks
Washington, D.C. 20231
Sir:

In response to the outstanding Official Office Action dated
5 March 1990, please amend this application as follows:

In the Claims:

1. (Twice Amended) In a computer including a memory for
holding a sequence of instructions to be executed, logic for
accessing the instructions in sequence, logic for determining
for each instruction a function to be performed and an effective
address thereof, and logic for executing each instruction, the
improvement wherein:

a)   the logic for executing instructions comprises:
a plurality of individual elements on a
common support substrate optimized to perform certain logical
sequences employed in executing instructions; and [additionally
comprising,]

b)   the computer comprises:
element selection logic means connected to
the logic determining the function to be performed for each

100 PT 08/16/90 103192   1 216   90.00 CK

1